



Features

- Shorter recovery time
- High speed switching
- High surge current capability
- Enabling higher frequency and increased power density
- System efficiency improvement
- System cost and size savings due to the reduced cooling requirements

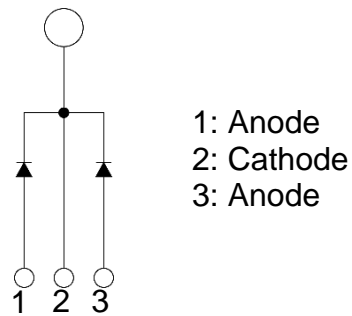
Outline (TO247-3L)



Applications

- Power Factor Correction in SMPS
- Solar inverter
- Uninterruptible Power Supply
- EV Charging Stations
- Data Center

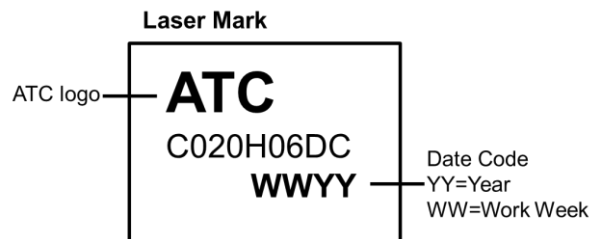
Circuit Diagram



Mechanical Characteristics

- TO247-3L package
- Halogen Free
- Pb free lead plating ; RoHS compliant
- Packaging: Tube

Marking Diagram





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Parameter and Specification

Absolute Maximum Rating⁽¹⁾

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V_{RM}	$T_c=25^{\circ}C$	650	V
Continue forward current	I_F	$T_c=135^{\circ}C$	20	A
Surge non-repetitive forward current , sine half-wave	I_{FSM}	$T_c=25^{\circ}C$, $t_p=10ms$, Sine half wave	153	A
		$T_c=110^{\circ}C$, $t_p=10ms$, Sine half wave	122	
Surge repetitive forward current	I_{FRM}	$T_c=25^{\circ}C$, $t_p=10ms$, Sine half wave	72	A
I^2t value	$\int I^2t$	$T_c=25^{\circ}C$, $t_p=10ms$, Sine half wave	117	A^2s
Total power dissipation	P_D	$T_c=25^{\circ}C$	162	W
		$T_c=110^{\circ}C$	70	
Junction temperature	T_j		175	$^{\circ}C$
Storage temperature	T_{STG}		-55 ~ 175	$^{\circ}C$

Note :

(1) Exceeding these ratings may damage the device.

Thermal Characteristics

Parameter	Symbol	Condition	Typ.	Unit	
Thermal resistance	θ_{jc}	Junction - Case	Per Leg	1.43	$^{\circ}C / W$
			Per Device	0.72	



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650V / 20A

SiC Schottky Barrier Diode

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Electrical Characteristics

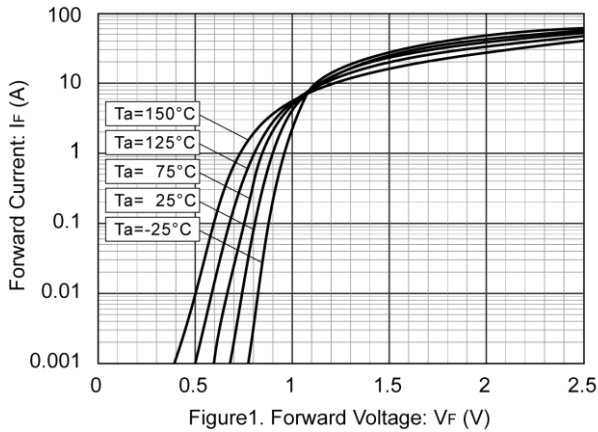
Characteristic	Symbol	Condition	MIN	TYP	MAX	Unit
DC reverse voltage	V_{DC}	$T_j = 25^{\circ}C, I_R = 2.0mA$	650	-	-	V
Forward voltage	V_F	$T_j = 25^{\circ}C, I_F = 20A$	-	1.35	1.5	V
		$T_j = 150^{\circ}C, I_F = 20A$	-	1.6	-	
		$T_j = 175^{\circ}C, I_F = 20A$	-	1.7	-	
Reverse current	I_R	$T_j = 25^{\circ}C, V_R = 650V$	-	4	100	uA
		$T_j = 150^{\circ}C, V_R = 650V$	-	16	-	
		$T_j = 175^{\circ}C, V_R = 650V$	-	30	-	
Total capacity charge	Q_C	$T_j = 25^{\circ}C, V_R = 400V,$ $di/dt = 350A/us$	-	57	-	nC
Total capacitance	C_{TOT}	$T_j = 25^{\circ}C, V_R = 1V,$ $F = 1MHz$	-	965	-	pF
		$T_j = 25^{\circ}C, V_R = 400V,$ $F = 1MHz$	-	88	-	
		$T_j = 25^{\circ}C, V_R = 650V,$ $F = 1MHz$	-	87	-	
Capacitance Stored Energy	E_C	$V_R = 400V$	-	9.2	-	μJ



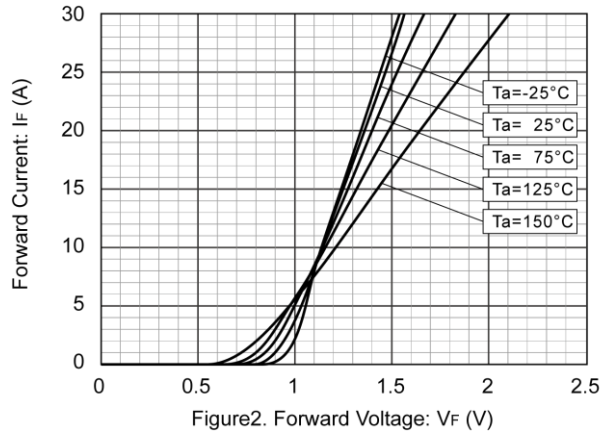
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Electrical Characteristic Curves

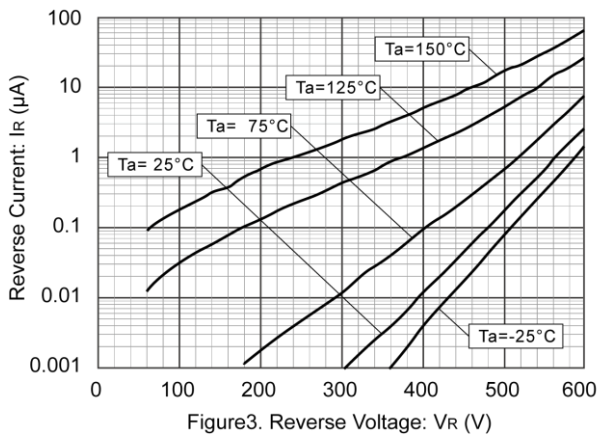
$V_F - I_F$ Characteristics



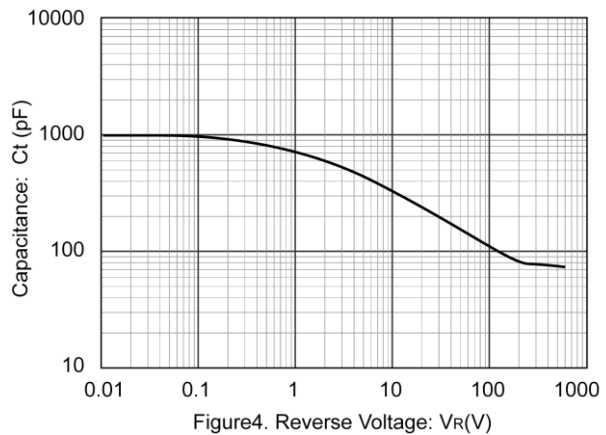
$V_F - I_F$ Characteristics



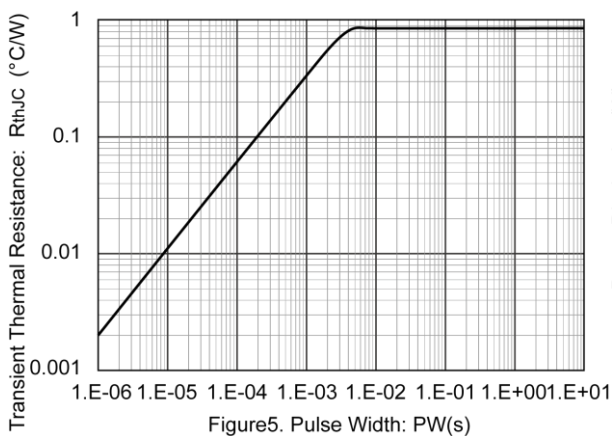
$V_R - I_R$ Characteristics



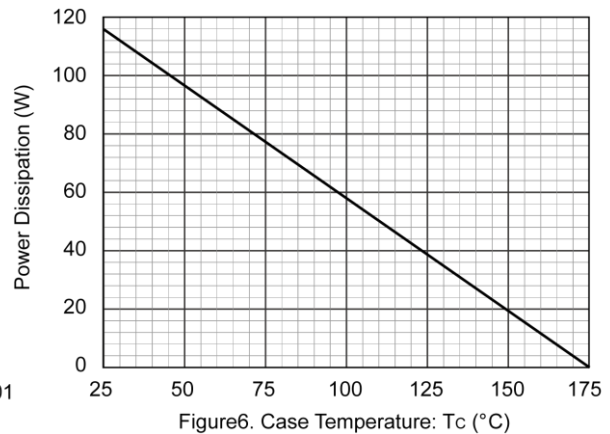
$V_R - C_t$ Characteristics



$R_{thJC} - PW$ Characteristics



Power Dissipation





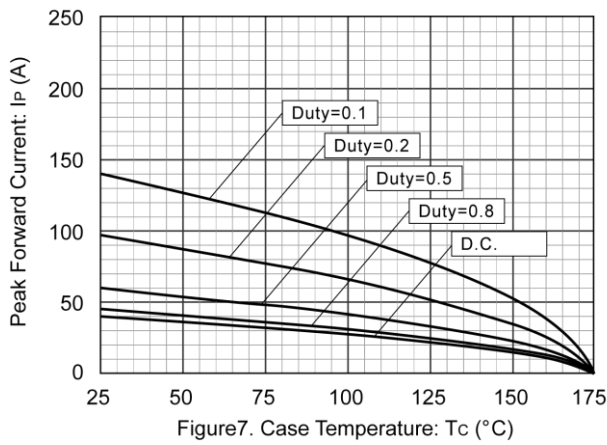
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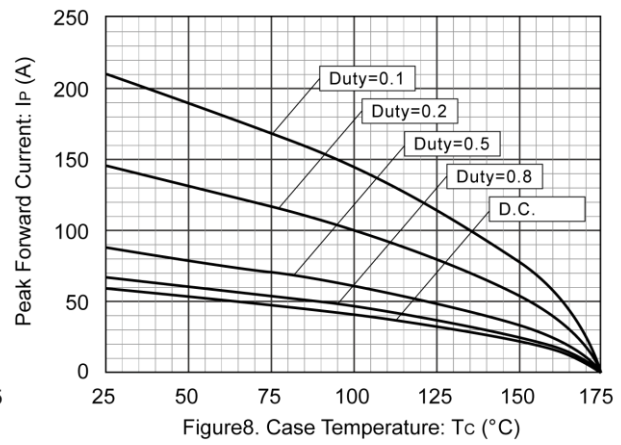
SiC Schottky Barrier Diode

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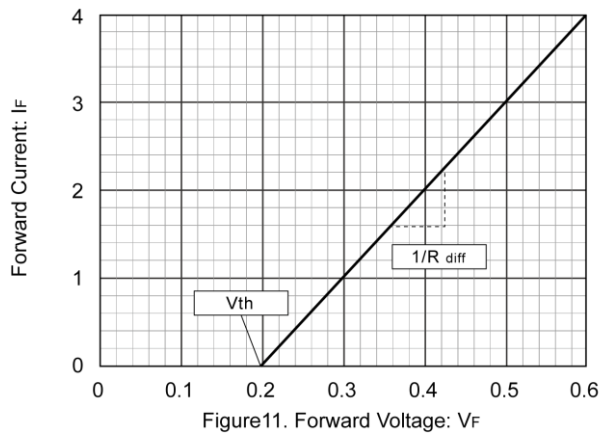
Maximum $I_p - T_c$ Characteristics



Typical $I_p - T_c$ Characteristics



Equivalent Forward Current Curve



$$V_F = V_{th} + R_{diff} I_F$$

$$V_{th}(T_j) = a_0 + a_1 T_j$$

$$R_{diff}(T_j) = b_0 + b_1 T_j + b_2 T_j^2$$

Symbol	Typical	Unit
a_0	0.966	V
a_1	-0.0011	V/ $^{\circ}\text{C}$
b_0	0.0176	Ohm
b_1	3.73E-5	Ohm/ $^{\circ}\text{C}$
b_2	3.84E-7	Ohm/ $^{\circ}\text{C}^2$

T_j in $^{\circ}\text{C}$; $-55^{\circ}\text{C} < T_j < 175^{\circ}\text{C}$; $I_F < 40\text{A}$



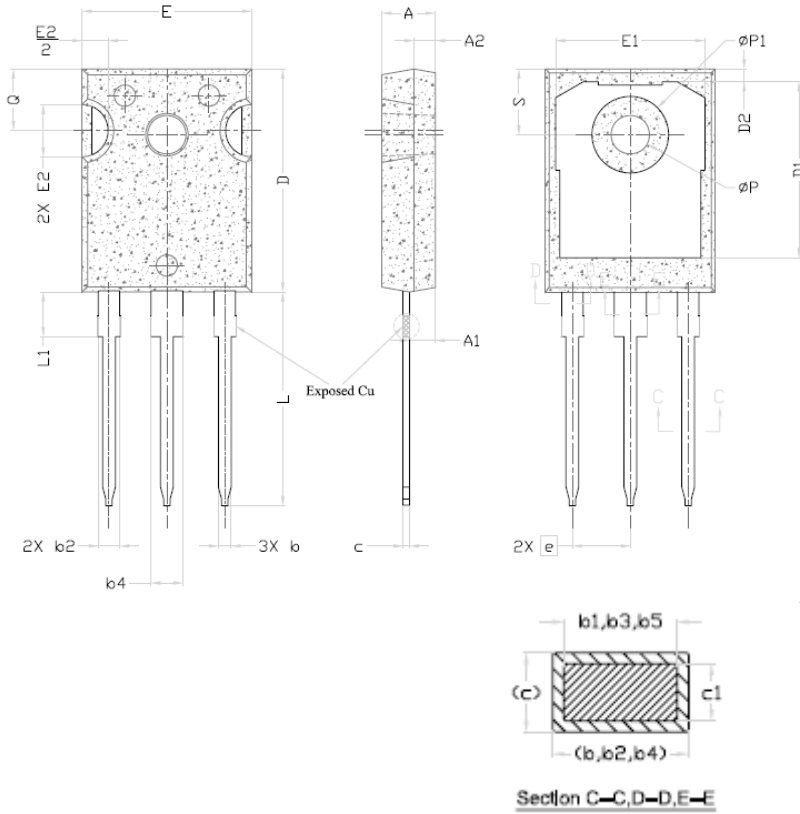
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SiC Schottky Barrier Diode

Package Outline



SYMBOL	DIMENSIONS		
	MIN.	NOM.	MAX.
A	4.83	5.02	5.21
A1	2.29	2.41	2.55
A2	1.50	2.00	2.49
b	1.12	1.20	1.33
b1	1.12	1.20	1.28
b2	1.91	2.00	2.39
b3	1.91	2.00	2.34
b4	2.87	3.00	3.22
b5	2.87	3.00	3.18
c	0.55	0.80	0.89
c1	0.55	0.80	0.85
D	20.80	20.95	21.10
D1	16.25	16.55	17.85
D2	0.51	1.19	1.35
E	15.75	15.94	16.13
E1	13.46	14.02	14.16
E2	4.32	4.91	5.49
e	5.44BSC		
L	19.81	20.07	20.32
L1	4.10	4.19	4.40
ØP	3.56	3.61	3.65
ØP1	7.19REF.		
Q	5.39	5.79	6.20
S	6.04	6.17	6.30